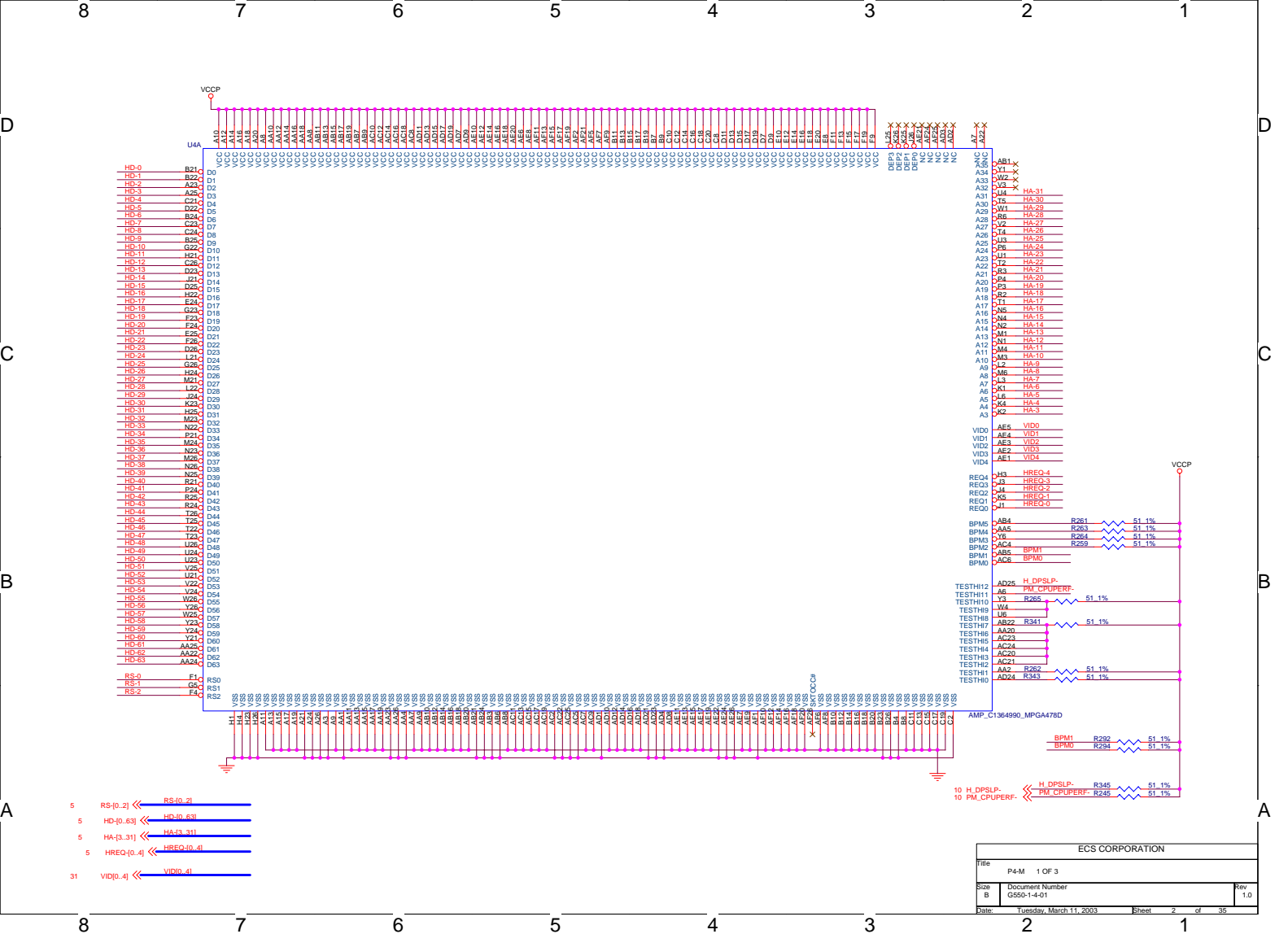


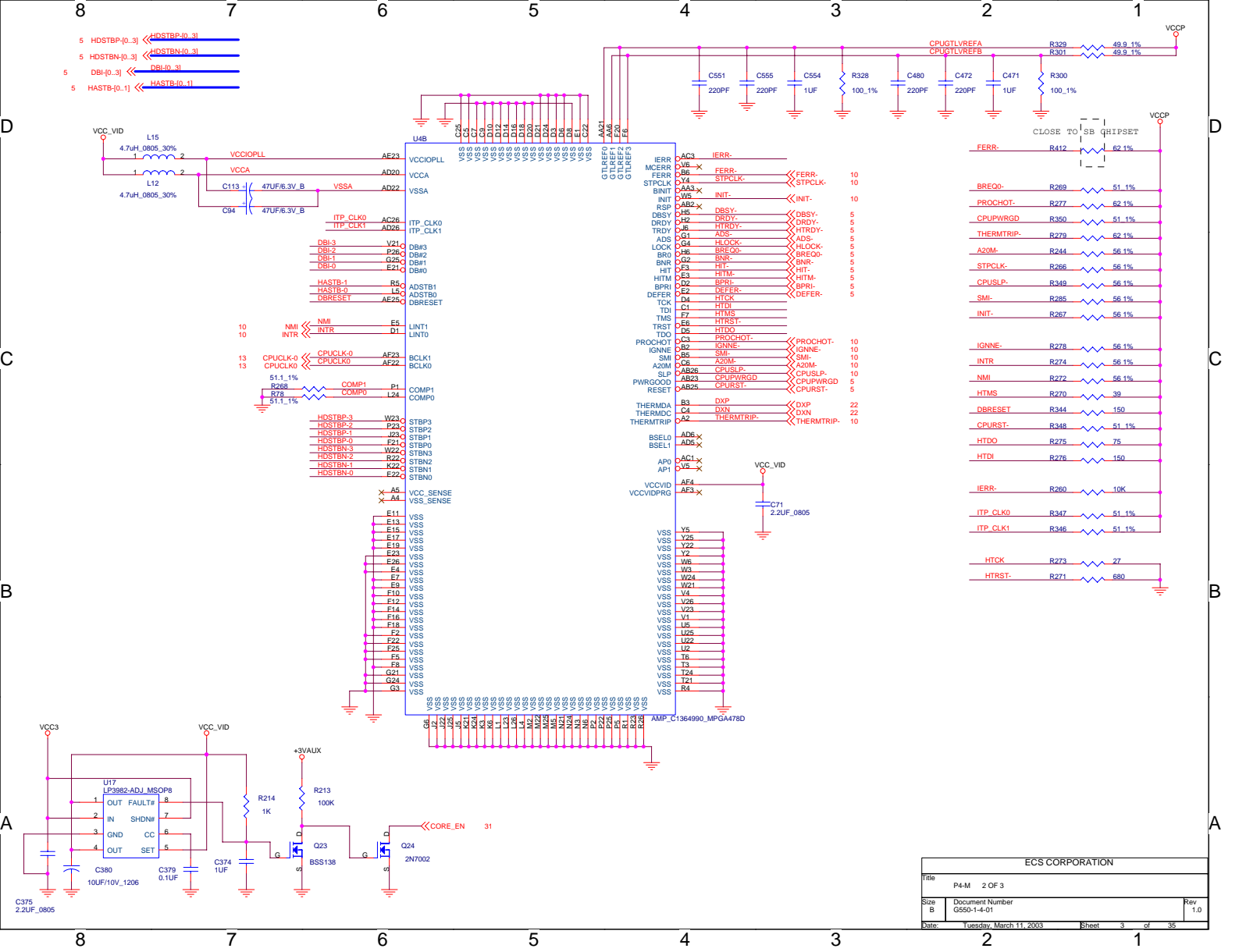
PAGE	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
REV	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
DATE	03/11/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03

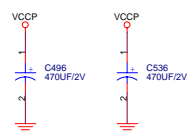
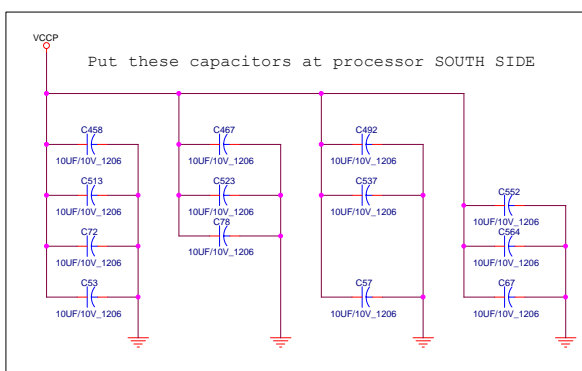
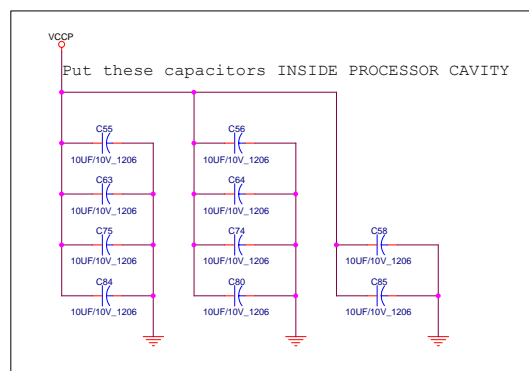
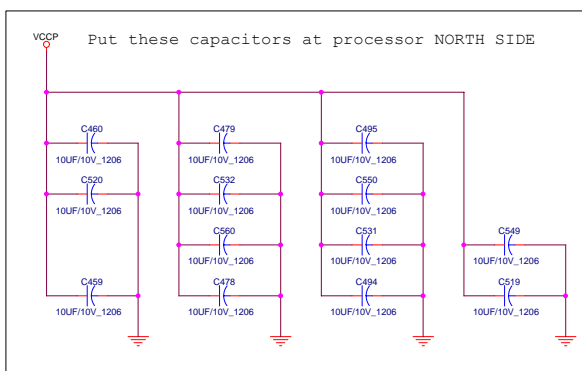
PAGE	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35					
REV	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0					
DATE	8/29/02	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	01/02/03	03/11/03				

P/N : KK0G550011010

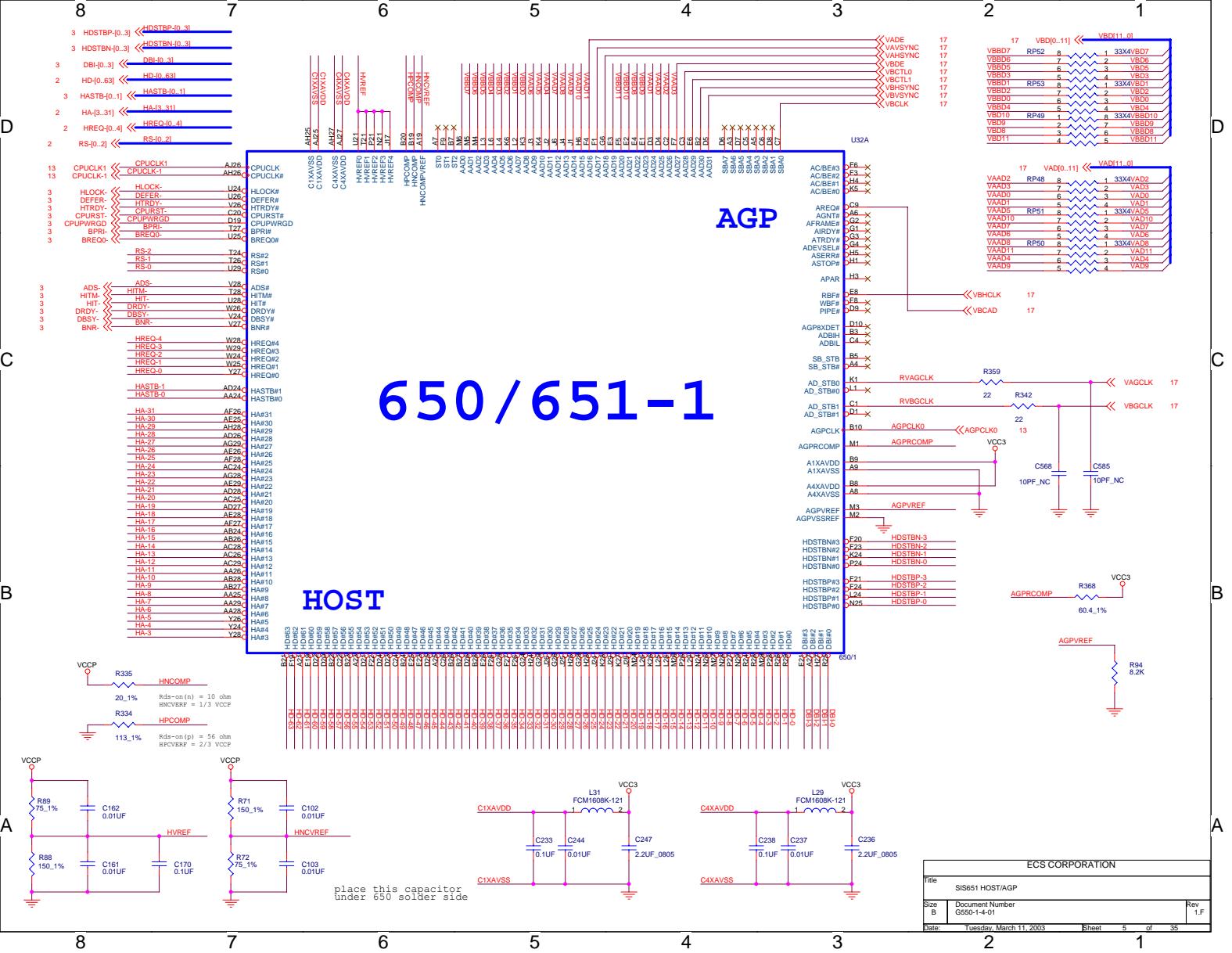
Approved	by	Checked	by	Designed	by
ECS CORPORATION					
File	G550.MIB				
Size	Document Number				
C	G550-1-4-01				
Rev	1.0				
Date	Issued: March 11, 2003				
Page	1 of 35				

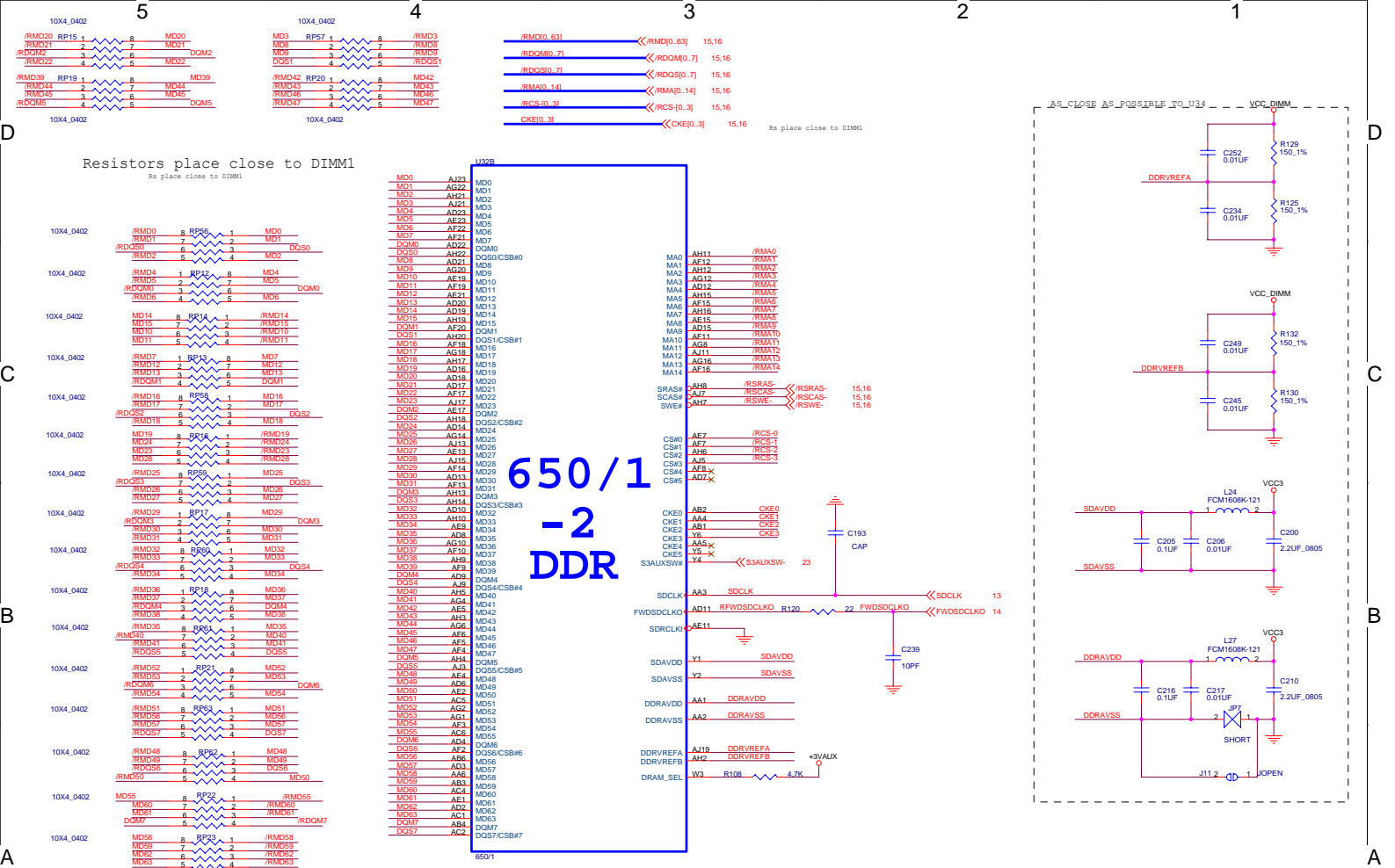


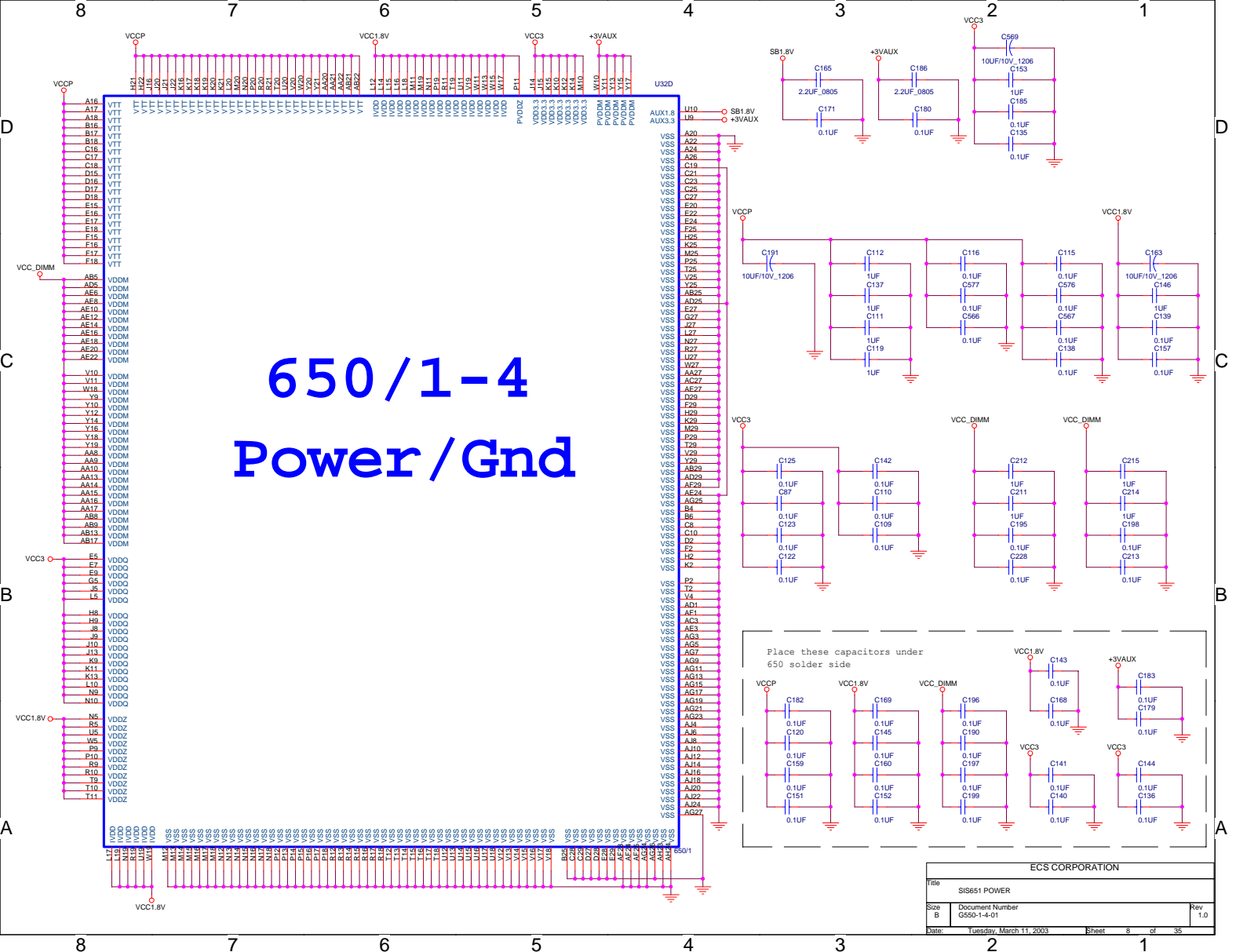


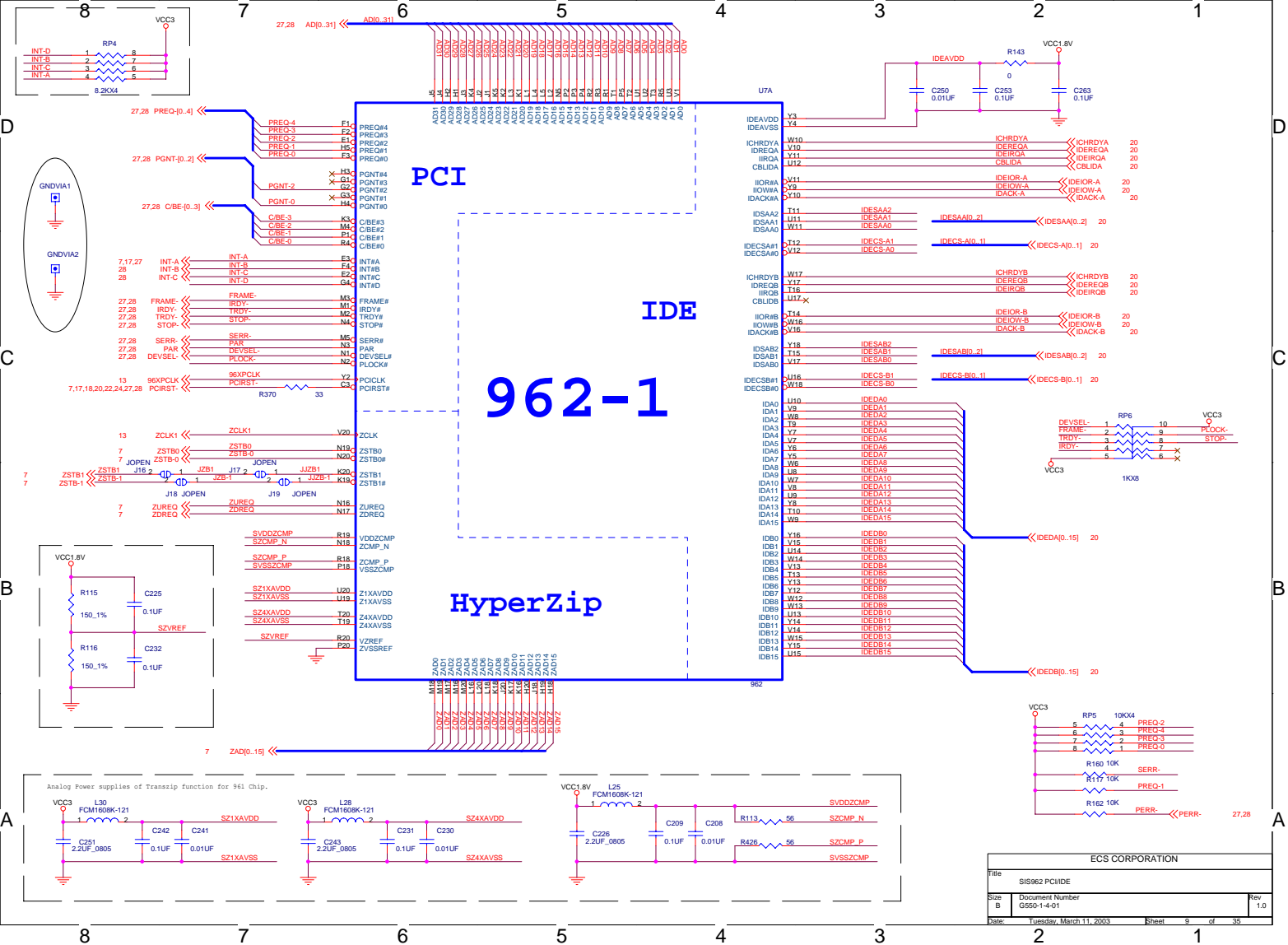


ECS CORPORATION				
Title P4-M 3 OF 3				
Size B	Document Number G550-1-4-01			Rev 1.0
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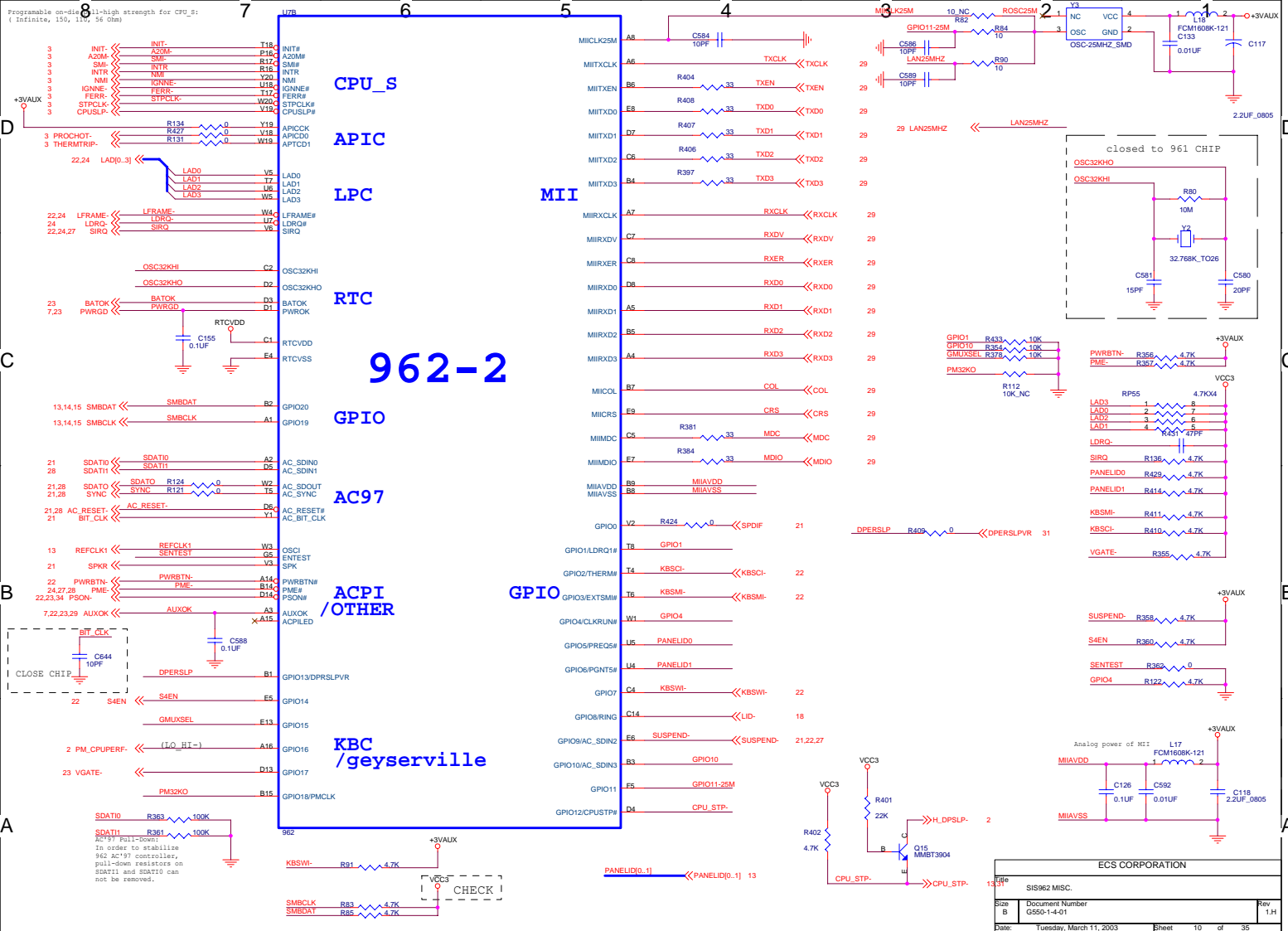






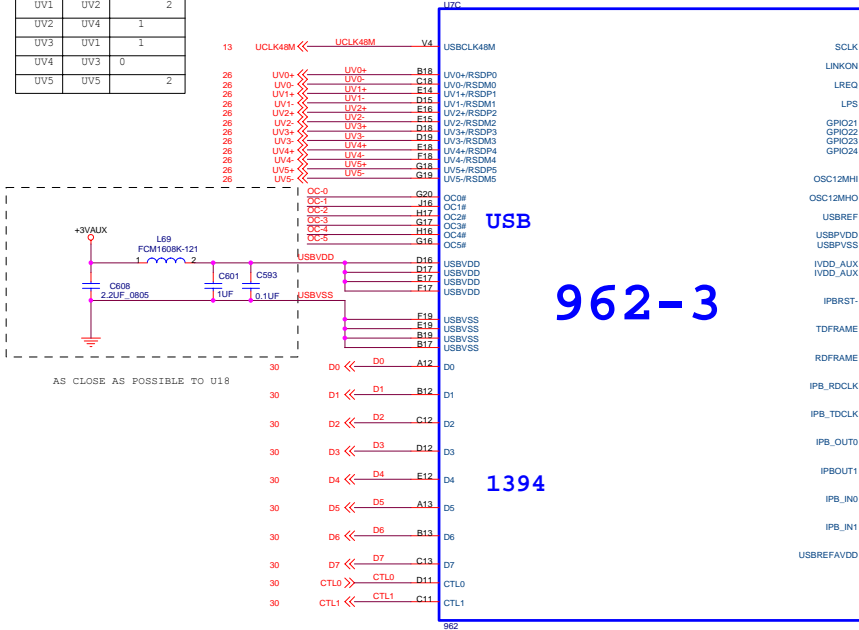


ECS CORPORATION			
Title SIS962 PC/IIDE			
Size B	Document Number G550-1-4-01		Rev 1.0
Date:	Tuesday, March 11, 2003	Sheet	9 of 35

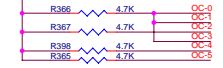


Note: The SCHMATICS OF USB port define is 961 .

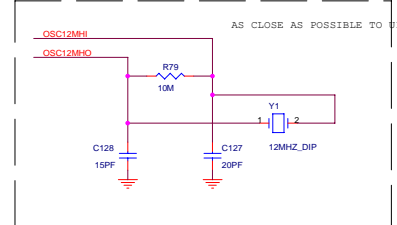
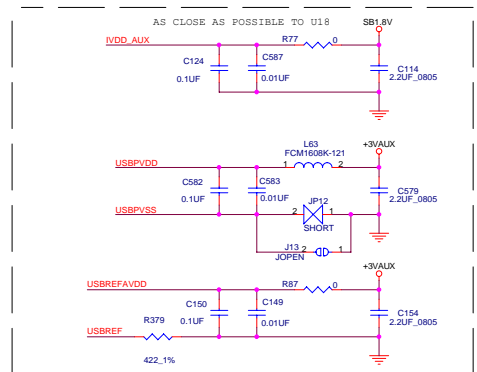
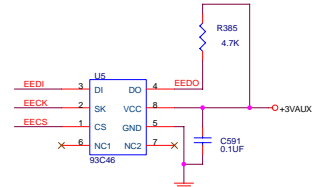
961	962	962 HOST
UV0	UV0	0
UV1	UV2	2
UV2	UV4	1
UV3	UV1	1
UV4	UV3	0
UV5	UV5	2



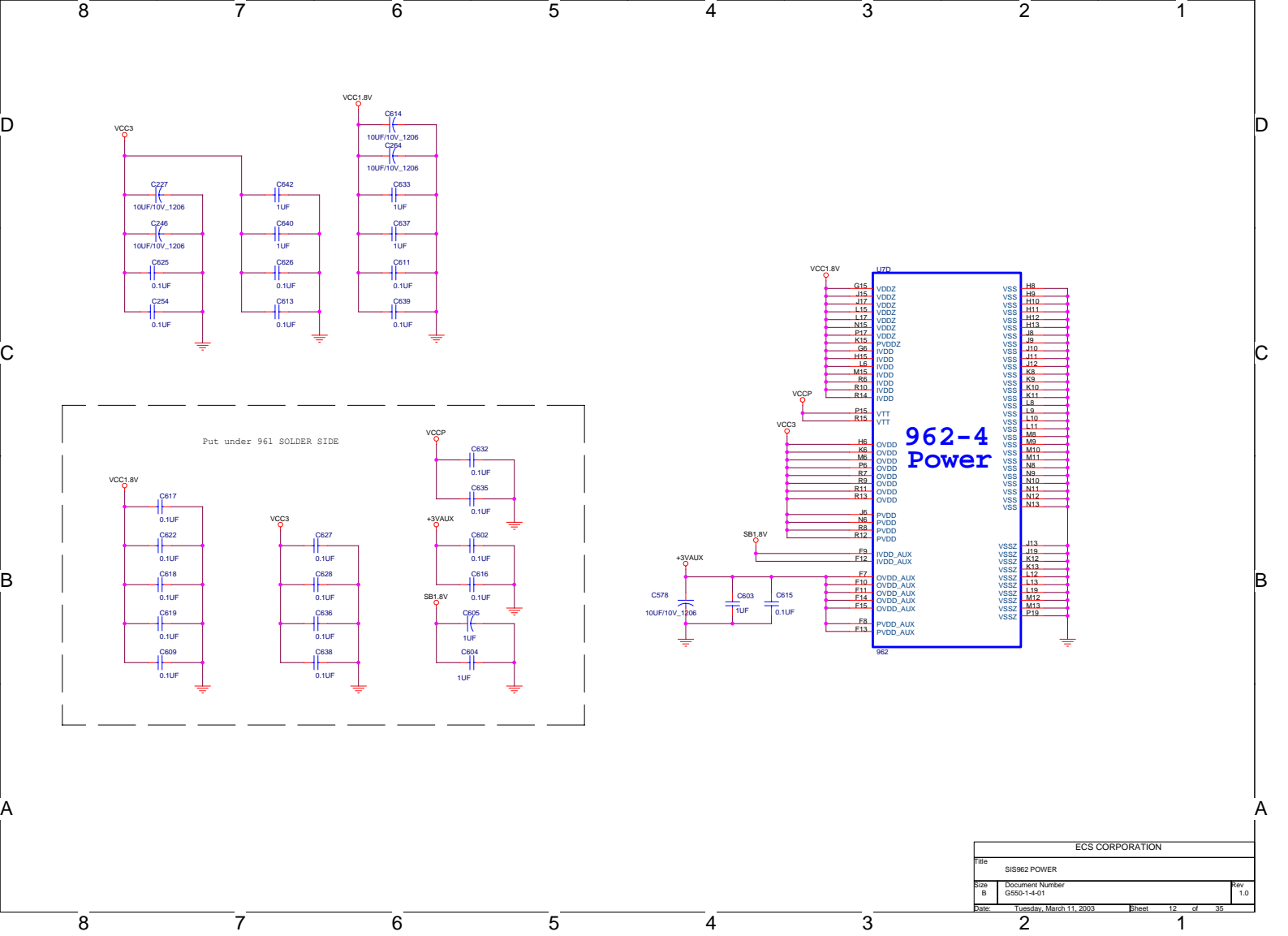
They don't need to be placed near the Si962



		0	1	Default
SPWR(L_FPC addr mapping)		disable	enable	0
SDAT0(FCTCLK PLL)		enable	disable	0
OC2-(I_BB debug mode)		enable	disable	1
OC3-(I_Trap mode)		PCI AD	ROM	1



ECS CORPORATION			
File	SIS962 USB CONTROLLER		
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ECS CORPORATION			
Title			
SIS962 POWER			
Size			
B	Document Number	G550-1-4-01	Rev
			1.0
Date: Tuesday, March 11, 2003			
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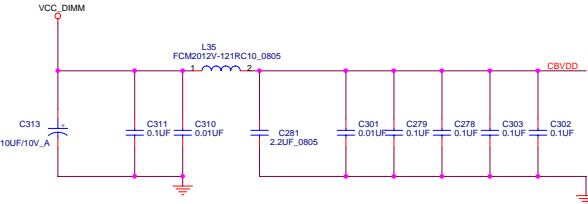
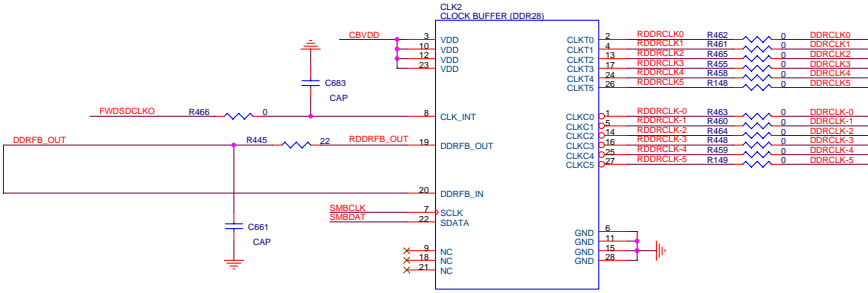
(3 OPTIONS)
1: (ICS)
2: (Cypress)
3. (Hitachi)

A

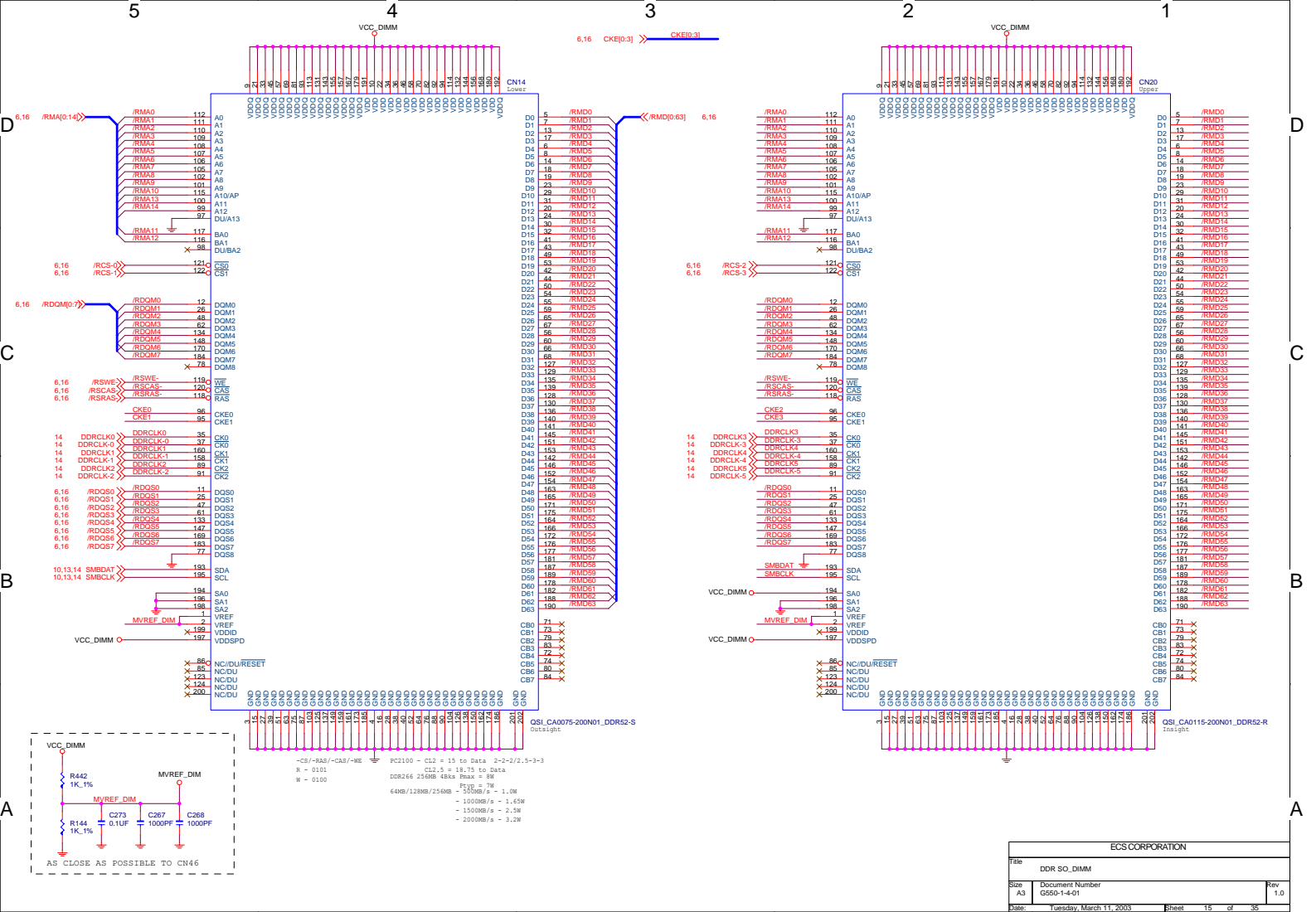
1

Clock Buffer (DDR)

(UPT10B3)
1: (DCS-93705)



ECS CORPORATION			
Title			
CLOCK BUFFER FOR DDR			
Size			
B	Document Number	G550-1-4-01	Rev
Date: Tuesday, March 11, 2003			
Sheet 14 of 35			
1.0			

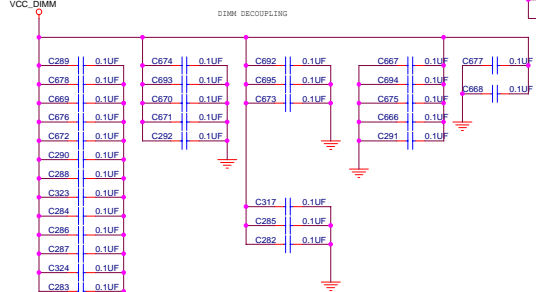
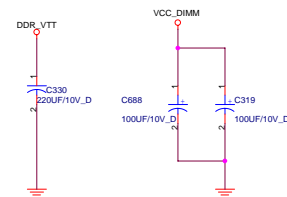
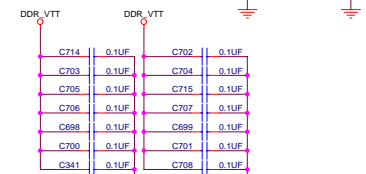
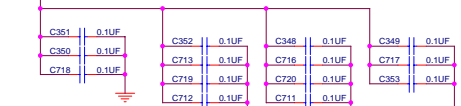
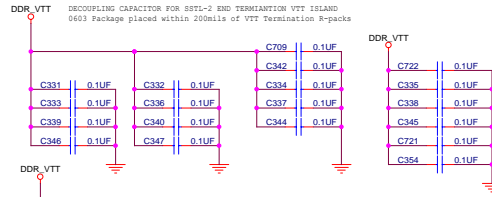
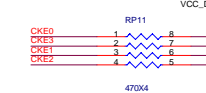
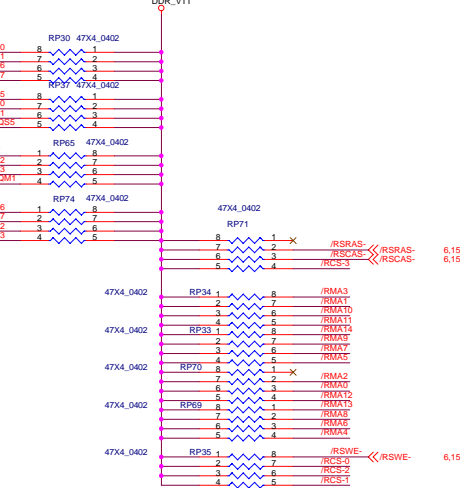
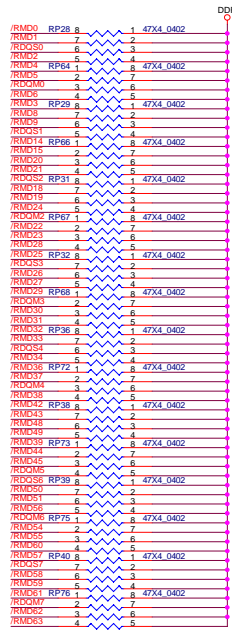
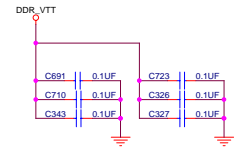


ECS CORPORATION		
File		
DDR SO_DIMM		
Size		
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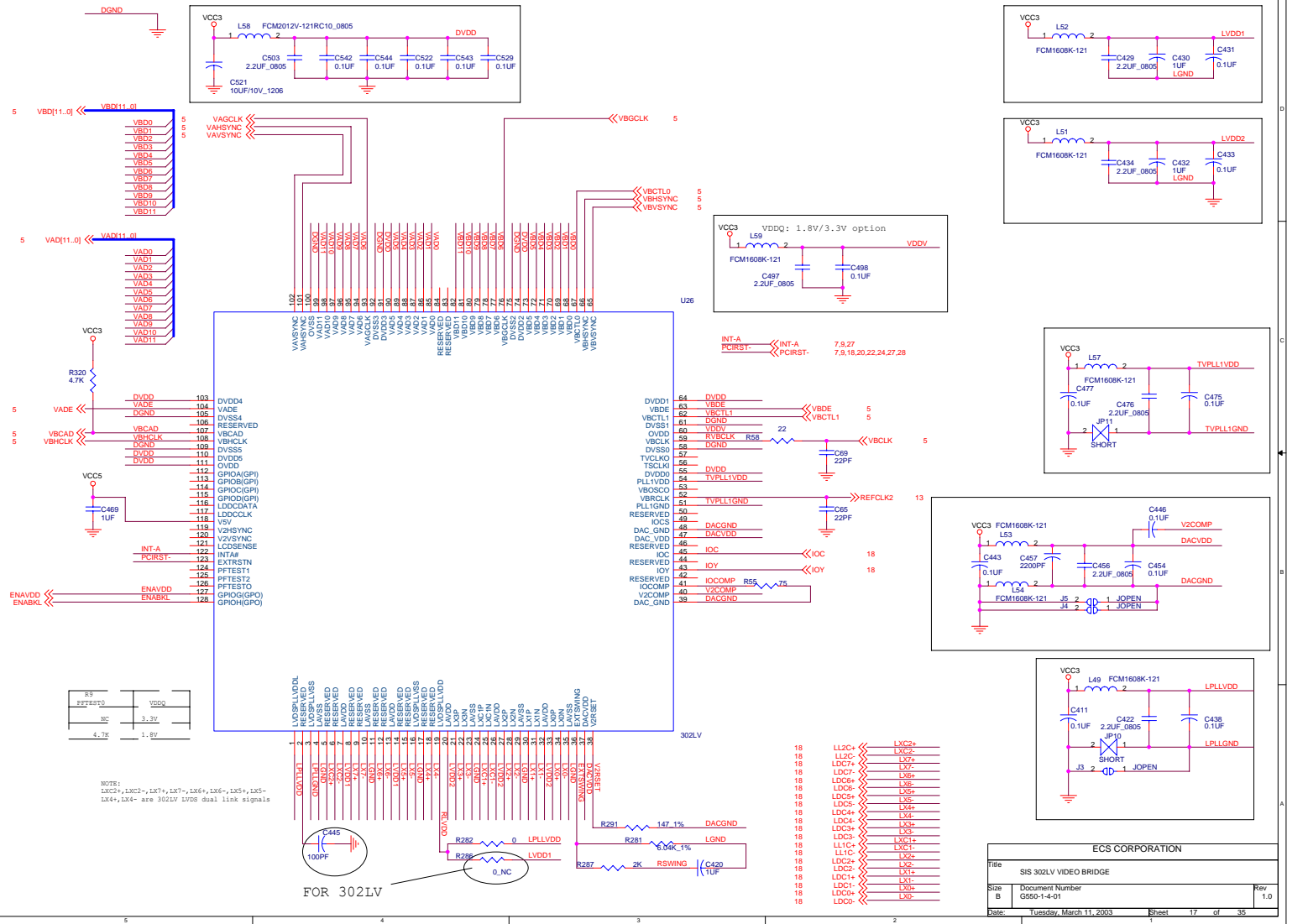
SSTL-2 Termination Resistors

/RMD0_63]	<</RMD0_63]	6,15
/RDQM0_7]	<</RDQM0_7]	6,15
/RDQS0_7]	<</RDQS0_7]	6,15
/RMA0_14]	<</RMA0_14]	6,15
/RCS0_3]	<</RCS0_3]	6,15
CKE0_3]	<</CKE0_3]	6,15

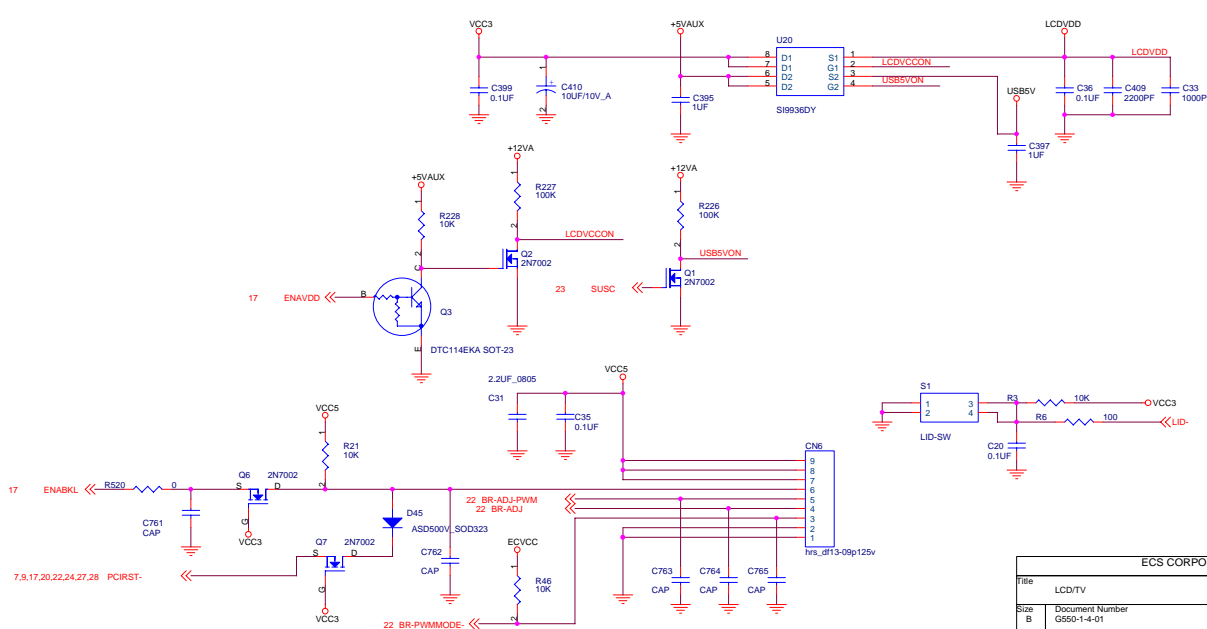
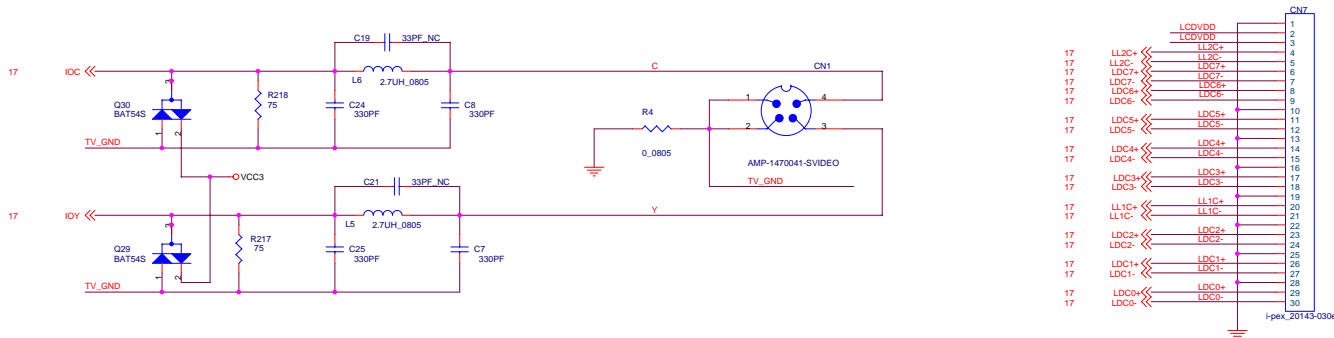
HS/DQM(0/28)	24~DQS	7/10/-	SSTL-2	Rs	8	8
RA/Control	24~DQS	10	SSTL-2	Rs	8	8
RA	24~DQS	10	SSTL-2	Rs	8	8
RA	24~DQS	10	SSTL-2	Rs	8	8



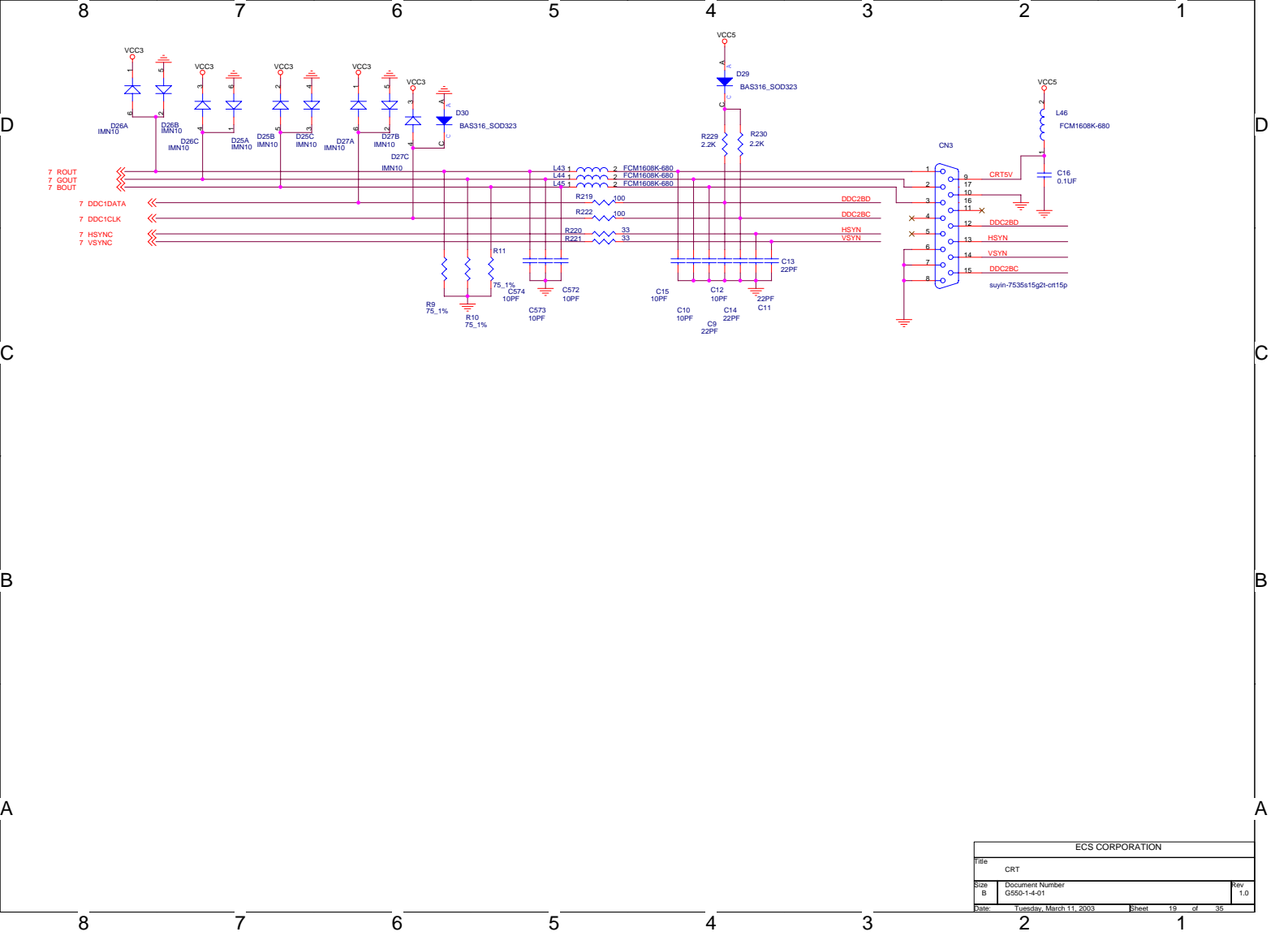
ECS CORPORATION			
File	DDR_RES_VTT		
Size	Document Number	Rev	
A3	G550-1-4-01	1.0	
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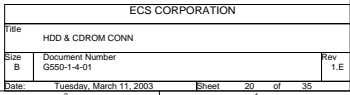


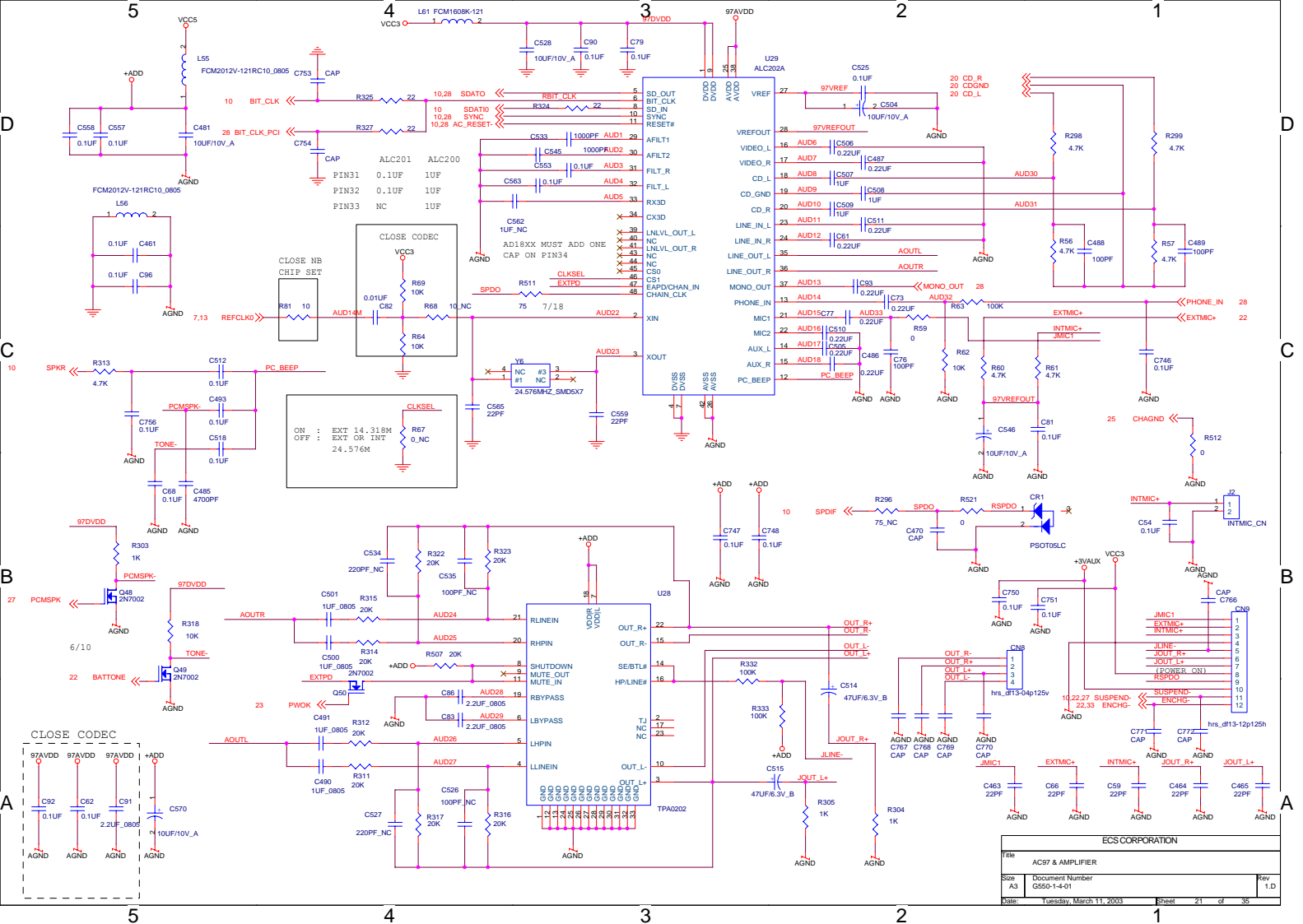
ECS CORPORATION			
Title			
SIS 302LV VIDEO BRIDGE			
Size			
Document Number			
G550-1-4-01			
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Tuesday, March 11, 2003			
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1.0			

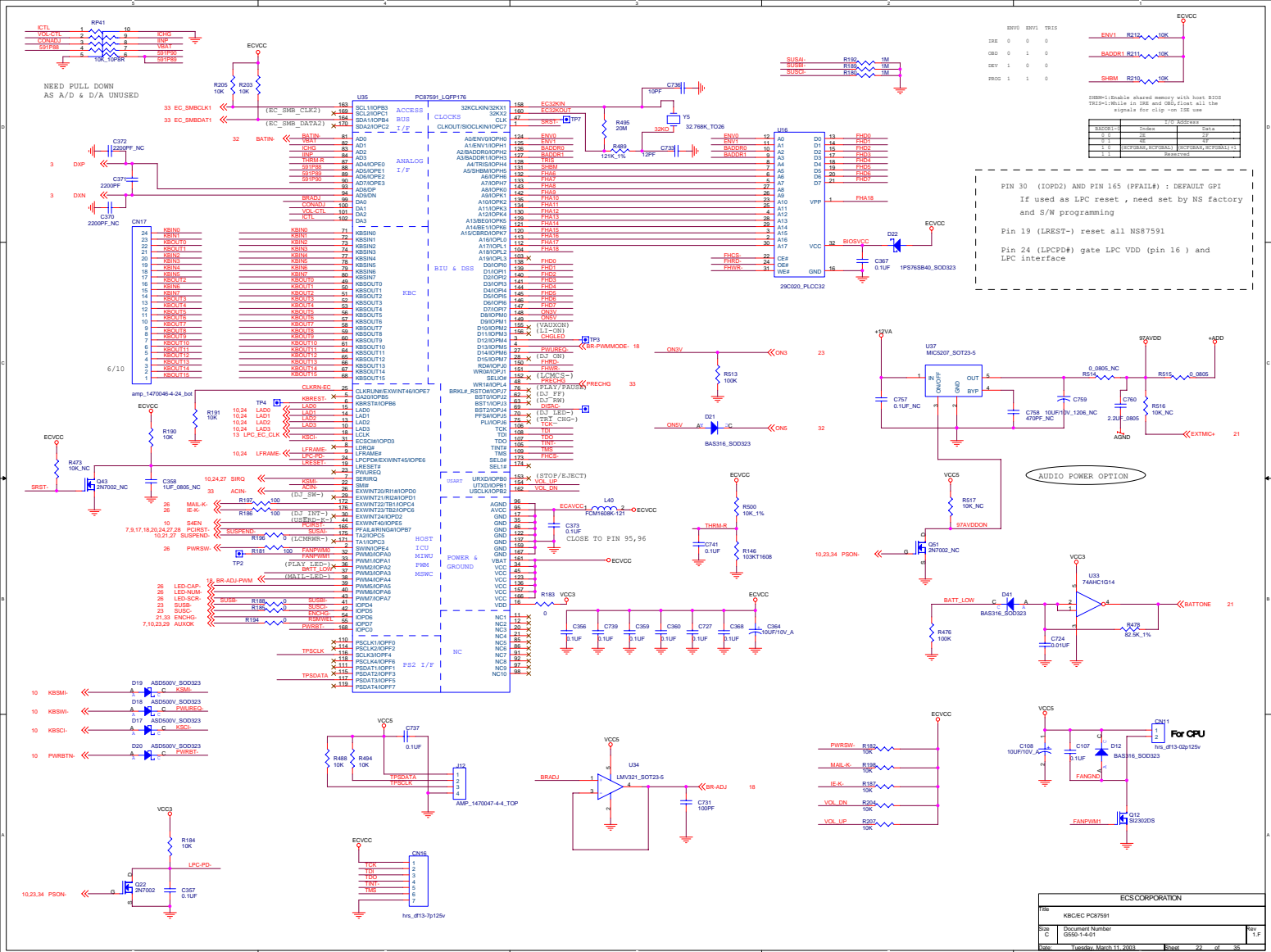


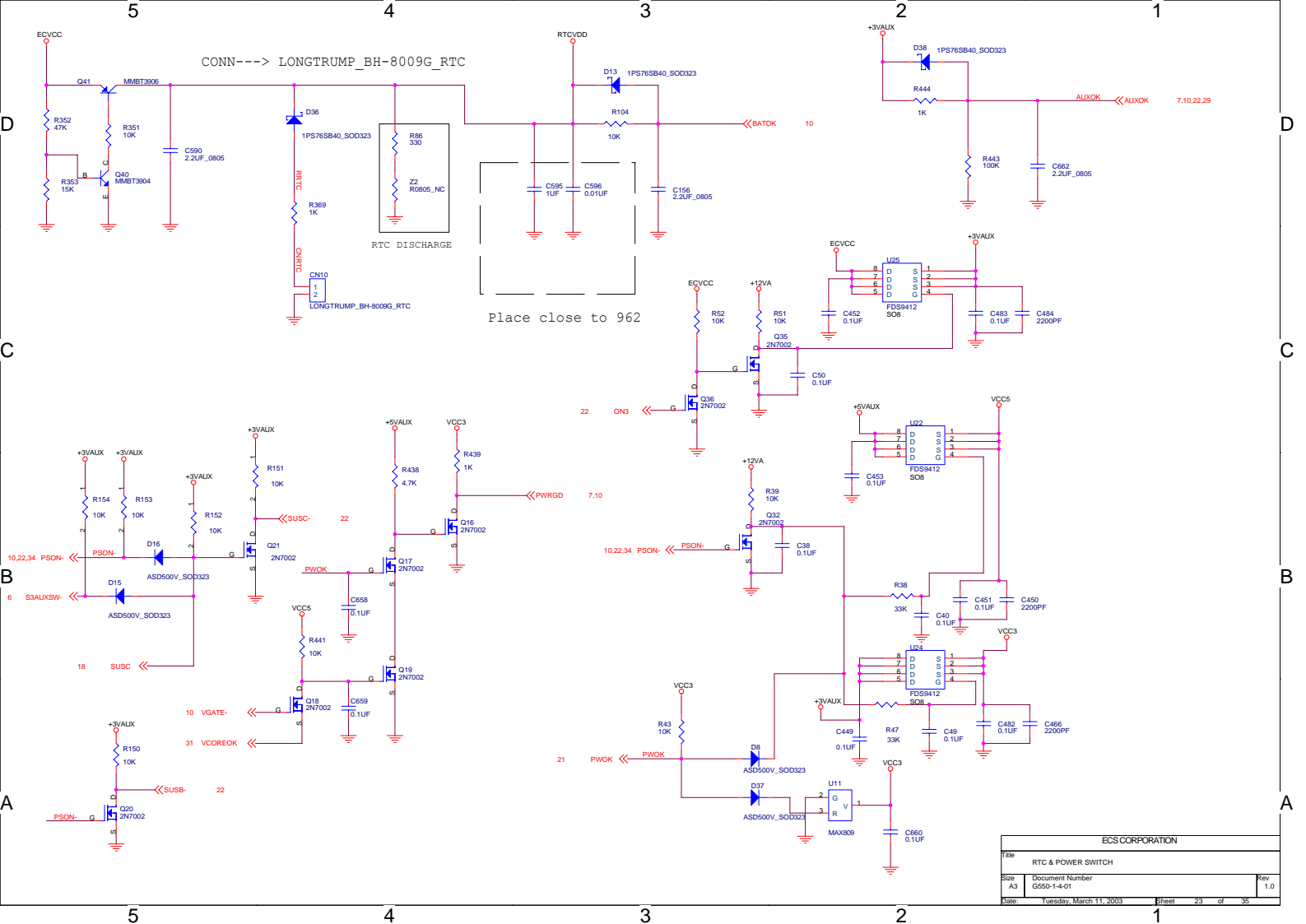
ECS CORPORATION			
Title	LCD/TV		
Size	Document Number	Rev	
B	G550-1-4-01	1.0	
Date:	Tuesday, March 11, 2003	Sheet	18 of 35

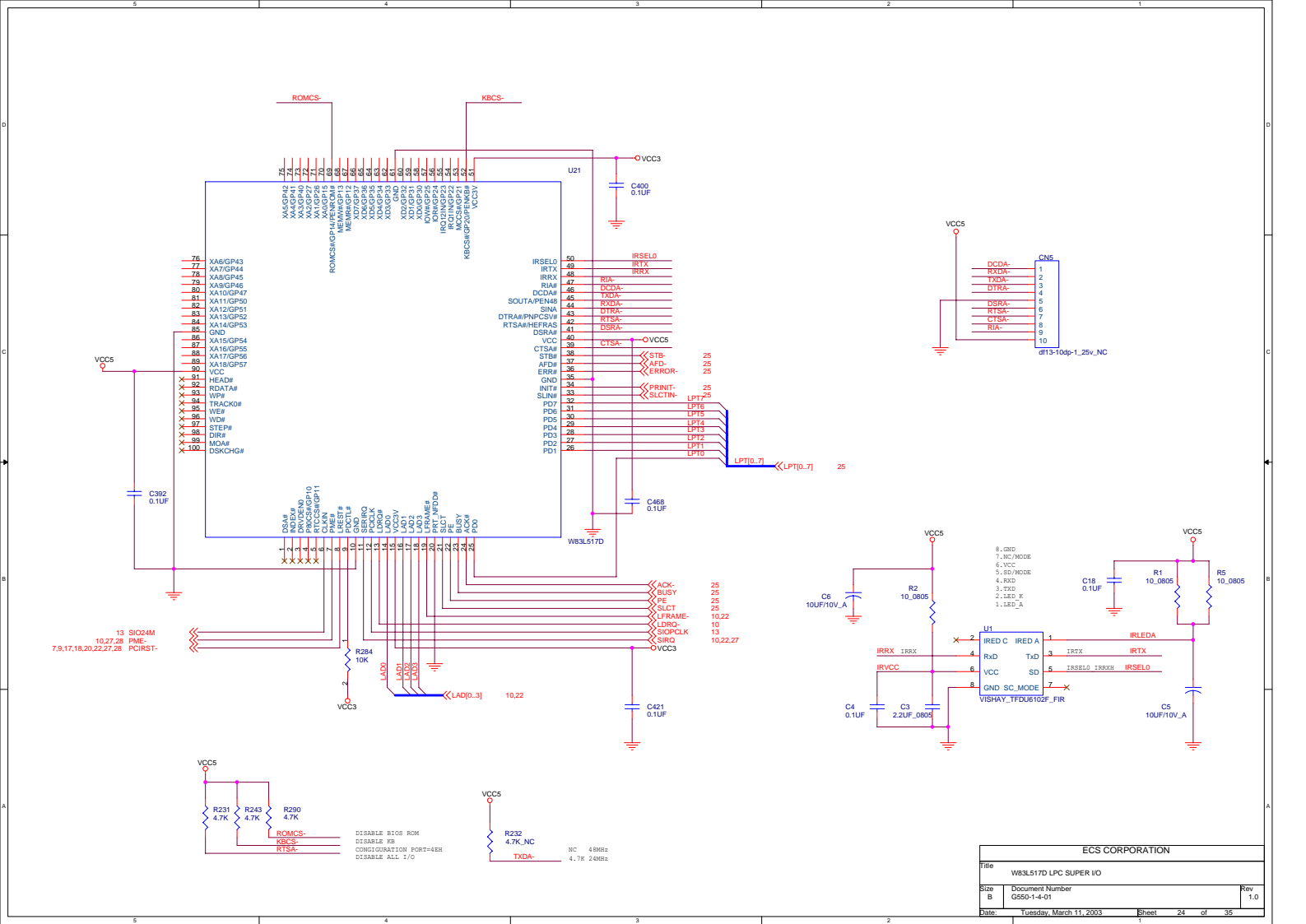


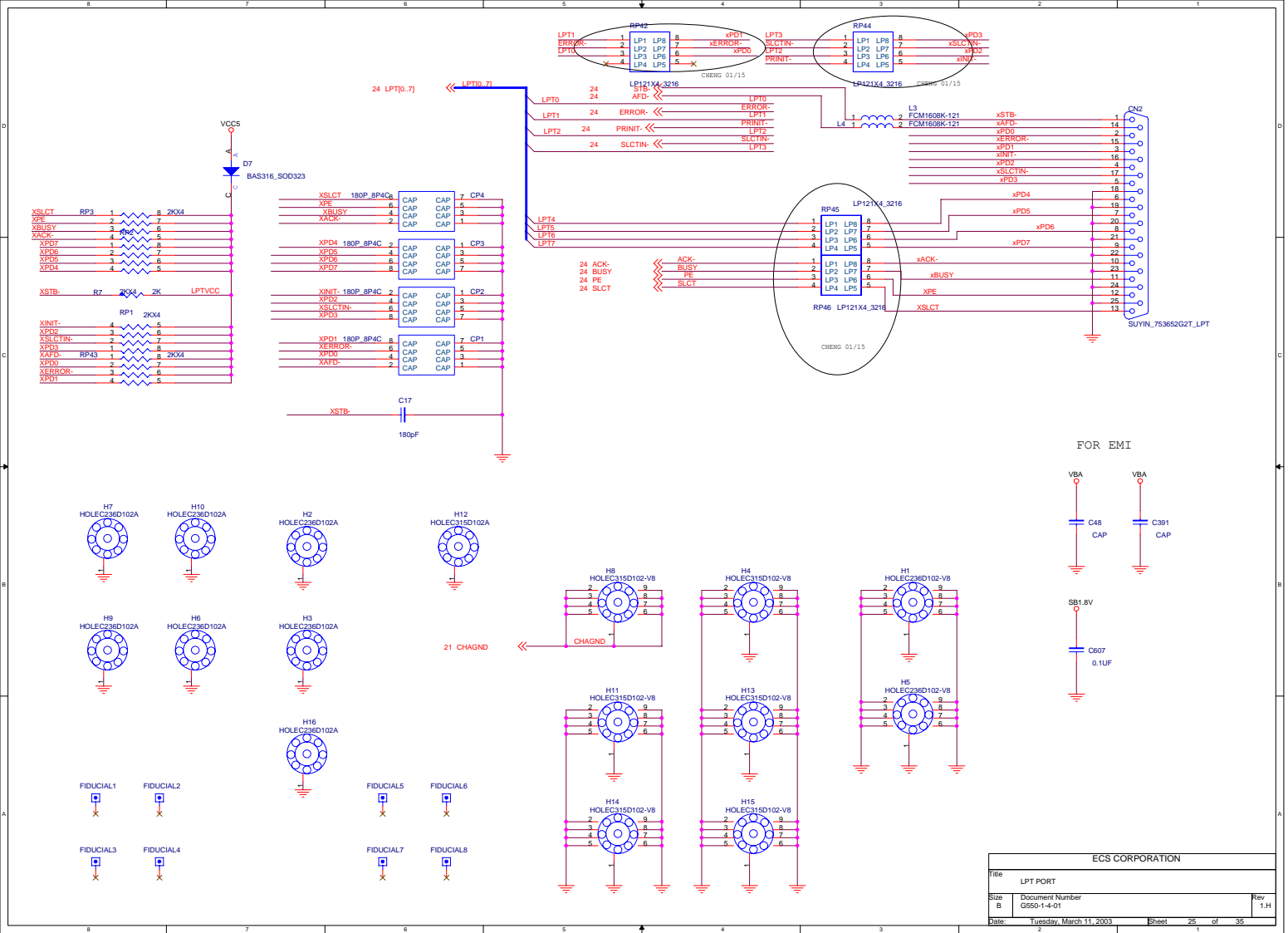


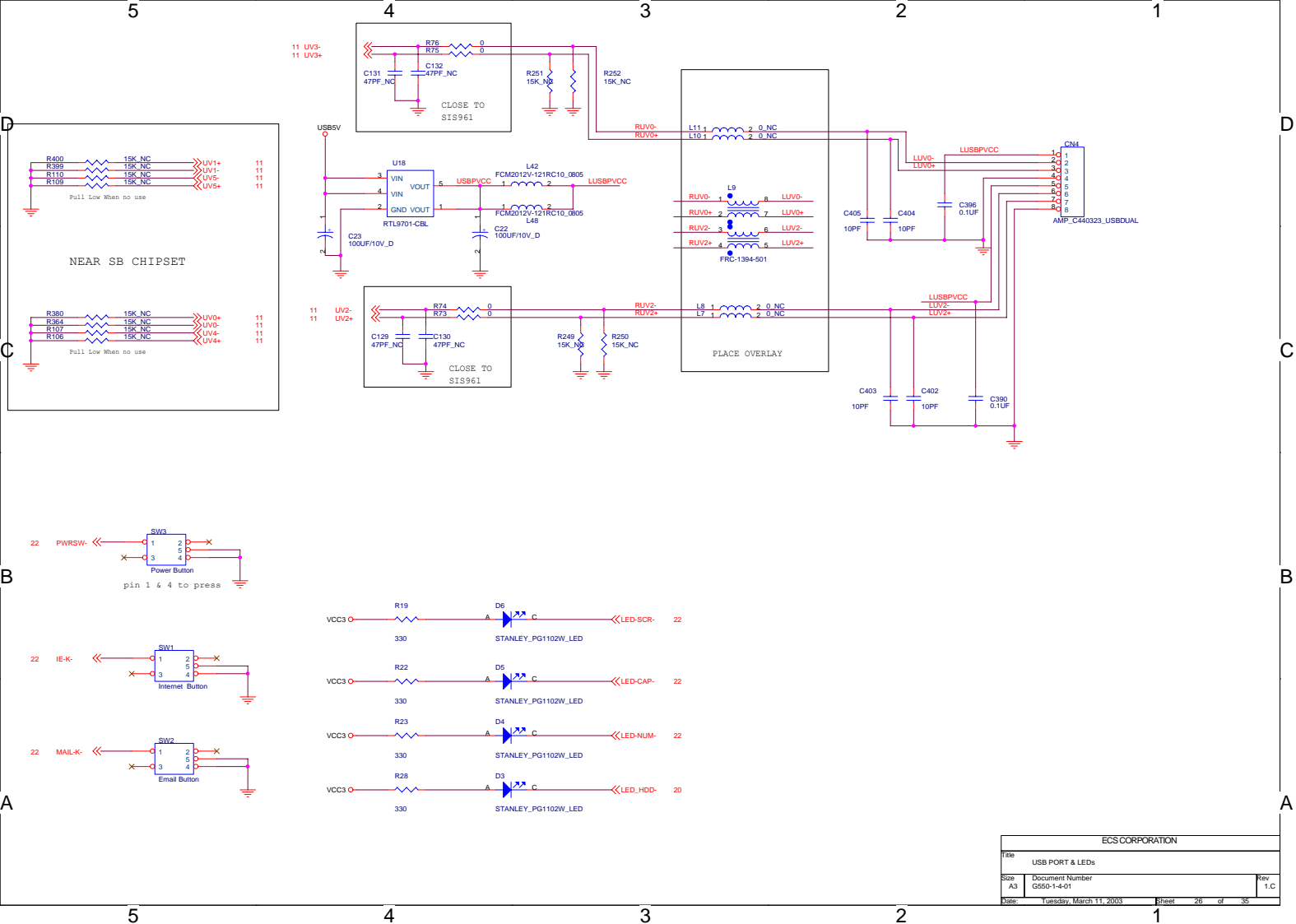


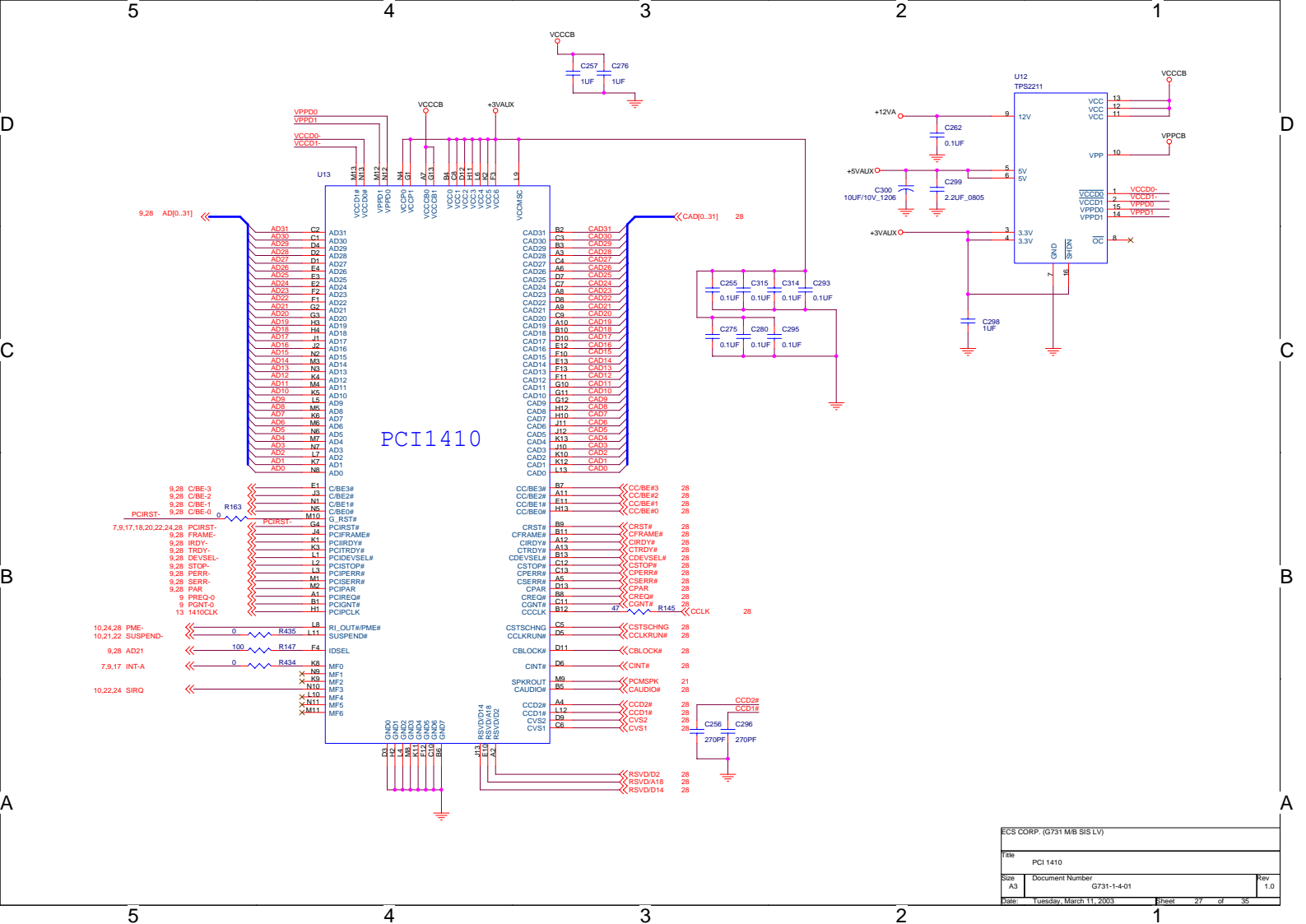


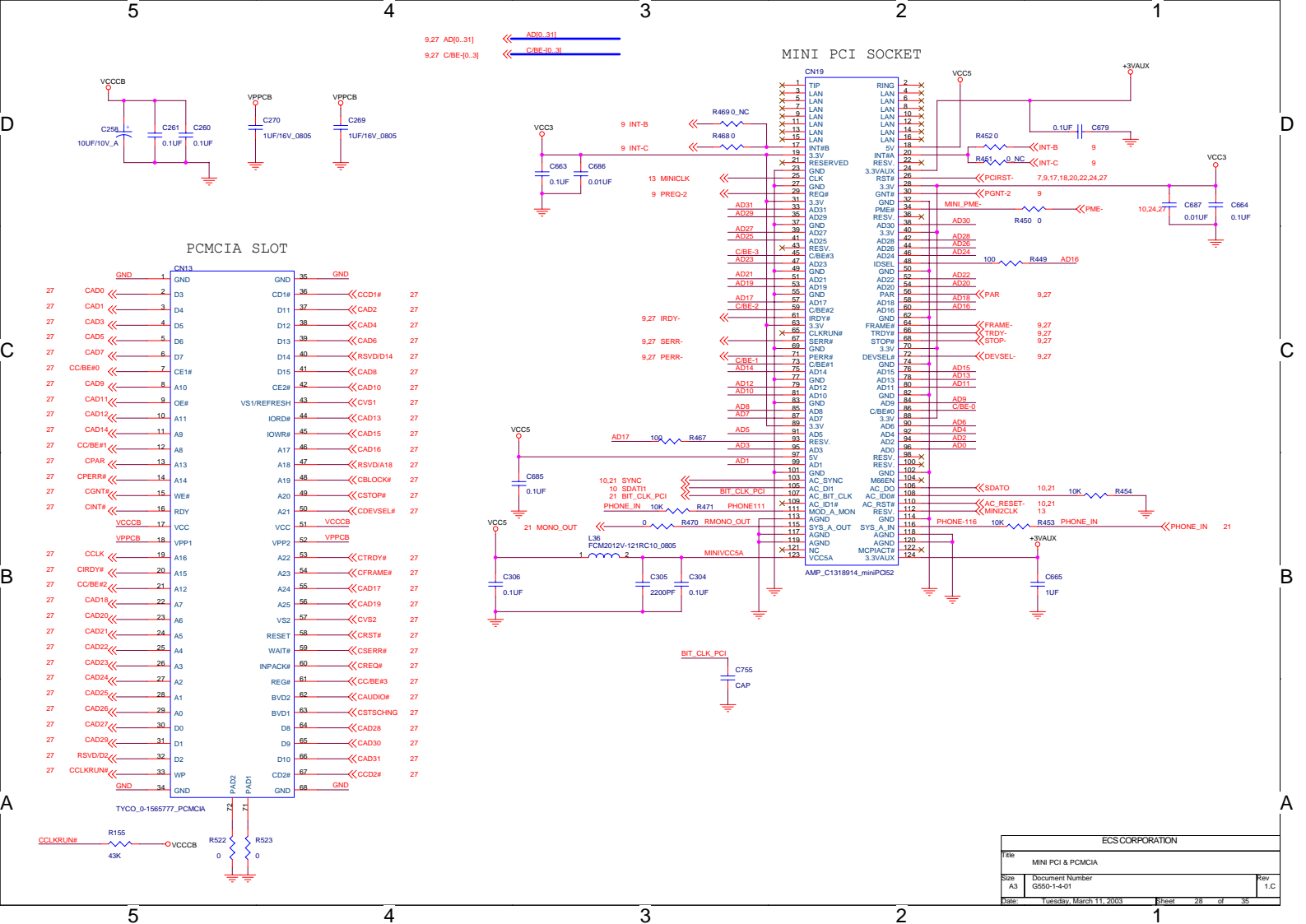




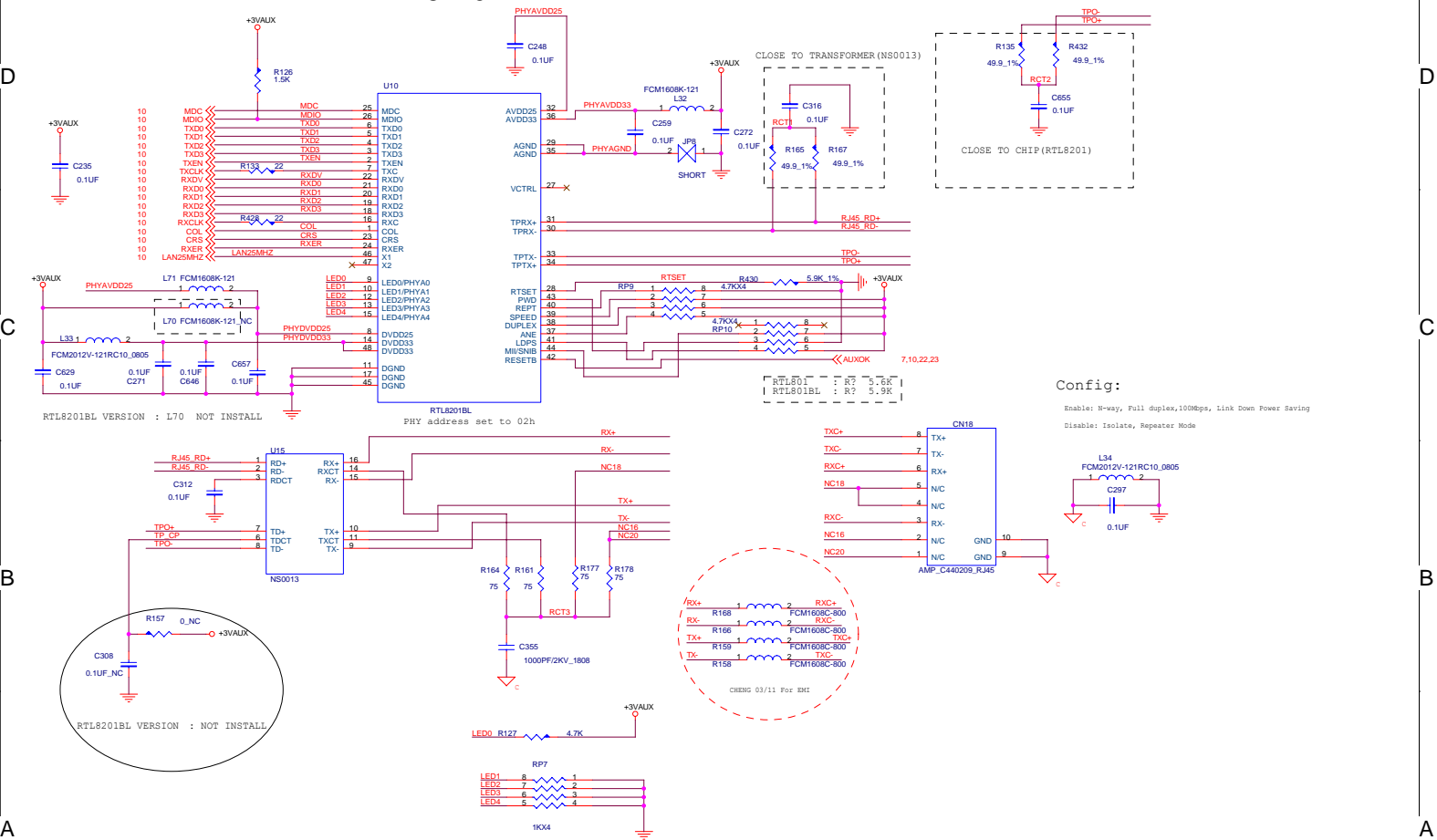




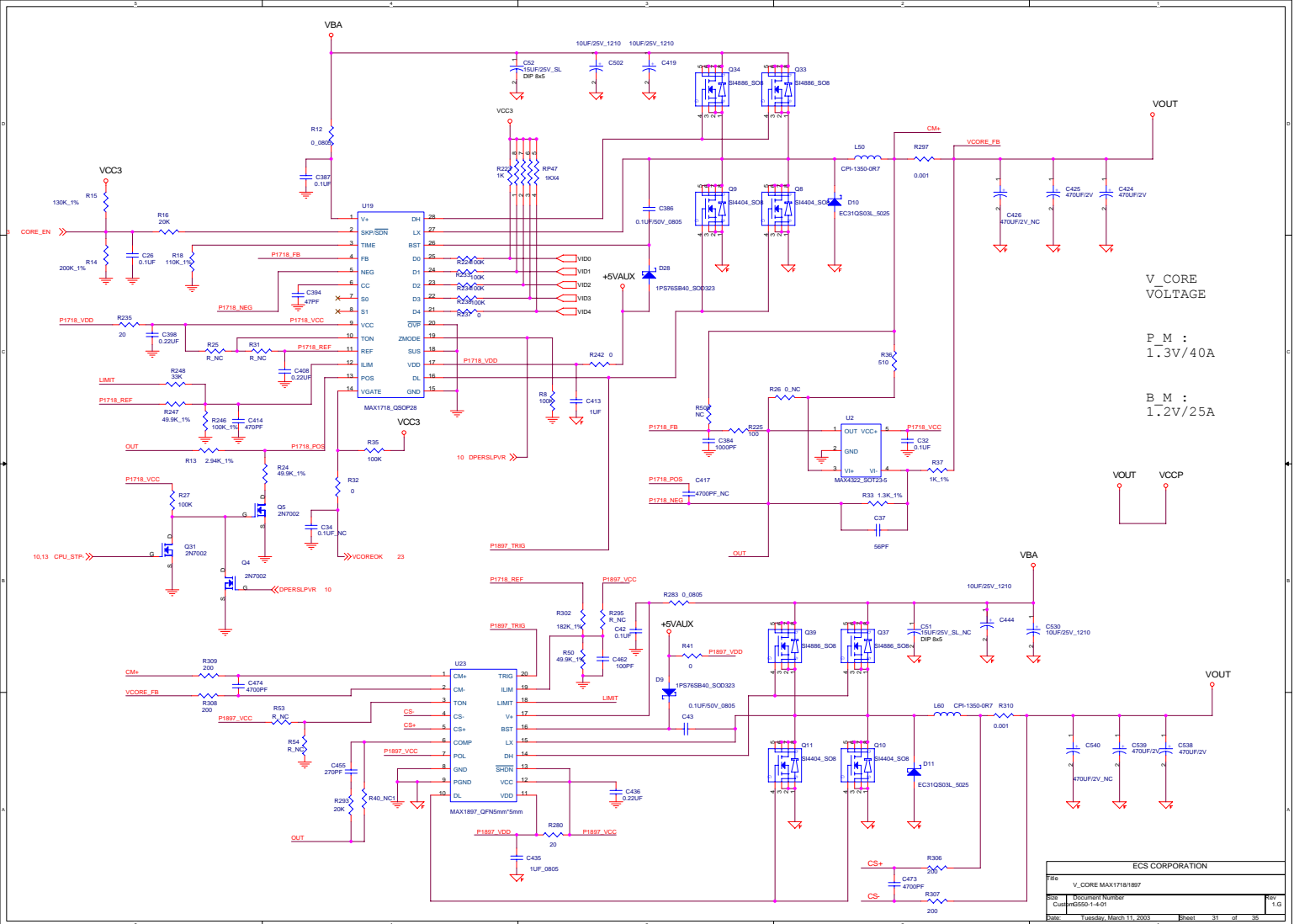


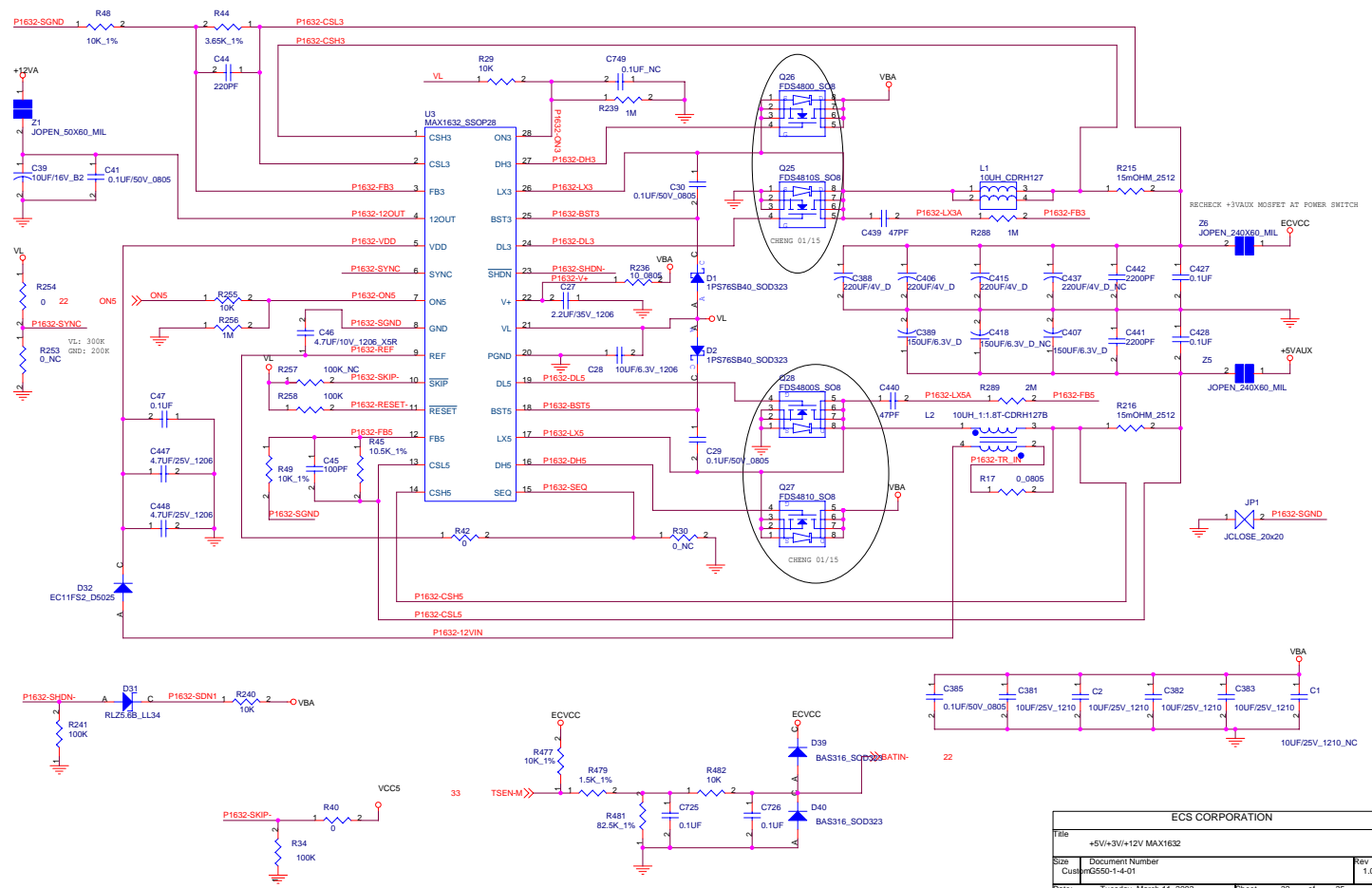


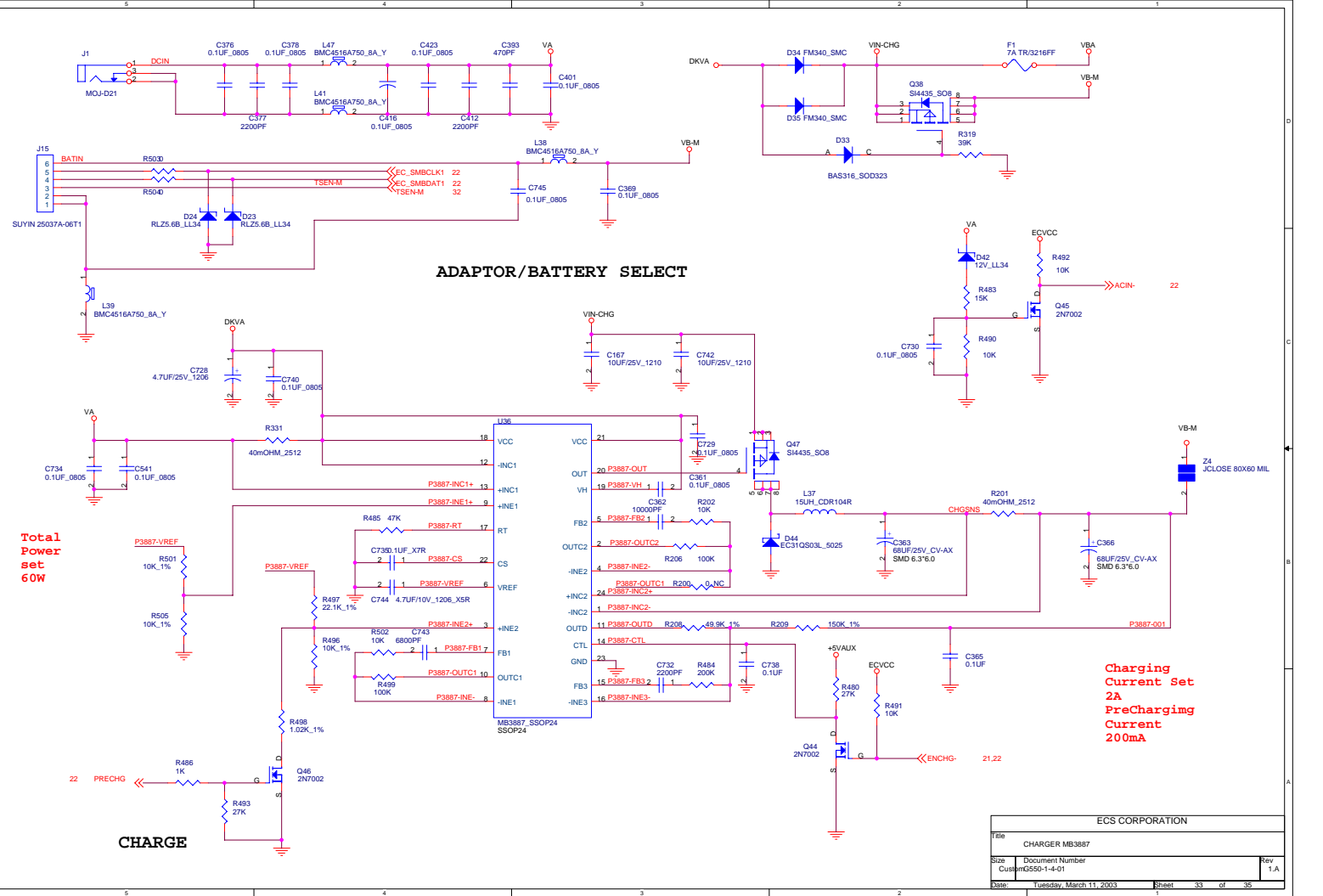
RTL8201

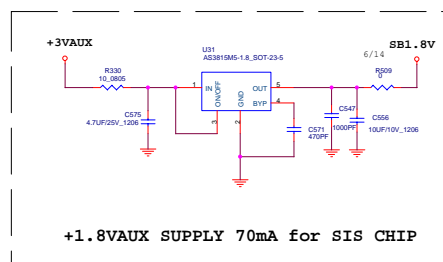
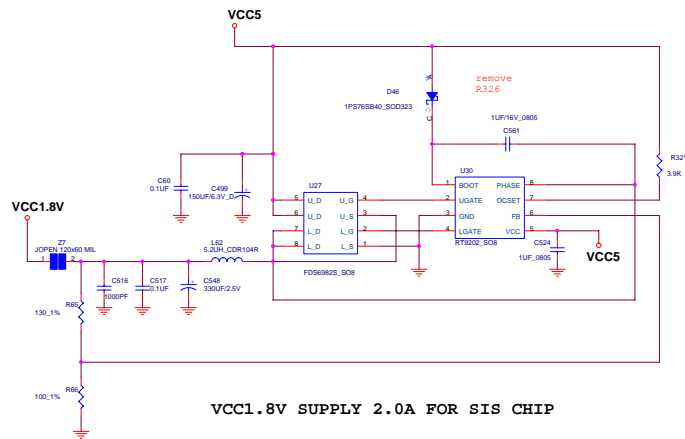
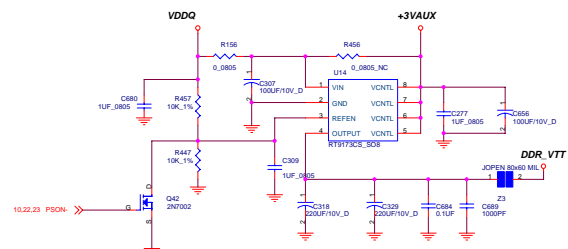
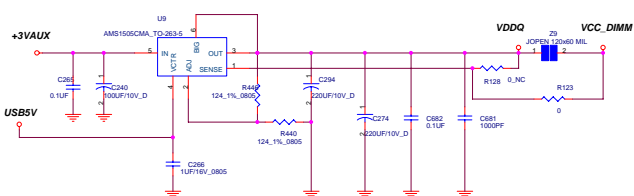


ECS CORPORATION			
Title RTL8201BL LAN PHY			
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Date	Tuesday, March 11, 2003	Sheet	29 of 35









- 54321
- 1.0 : Initial Release.
- 1.A : Fix EMS harmonic. Change R505 from 12.1kΩ to 10kΩ(Sheet 33 of 35)
- 1.B : To fix MIC function.Change R59 from 1kΩ to 0Ω,change R60,R61 from 2.2kΩ to 4.7kΩ,and change C54 from 470pF to 0.1uF(sheet 21 of 35)
- 1.C : Improve EMI for USB port and PCMCIA shelter.EMI solution to add 10PF for C402,C403,C404,C405 and add 0Ω for R522,R523
- 1.D : Change codec clock source.Delete R67 ,R68 and add Y6.
- 1.E : TO solve problem for OS sometime detect no device in IDE channel.
- 1.F : FIX 15" SXGA model overlap picture problem.Delete C568,C585 10pf.
- 1.G : To improve the high-frequency noise.
- 1.H : Fix EMI problem for LGA and IR DMA LOST.

(1)Chang R168,R166,R159,R158 form 0 Ohm to 120 Ohm bead

(2) ChangRP42,RP44,RP45,RP46 form 33 4-R Arrays to 120 4-B Arrays.

(3) Chang Q26,Q27 for FDS9412_S08 to FDS4800_S08 and Q25,Q28 form FDS6680_S08 to EF48100_S08.

(4) Add capacitor to C631.

(5) Chang R431 form 4.7k Ohm to 47pf capacitor.
- 1.J : Use bead to change EMI for lanport. Change R168, R166, R159, R158 from 120 Ω to 80 Ω bead.
- 0
- B
- C
- A

ECS CORPORATION			
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